

W&S – LCD Drivers Product Table

GRAPHIC LCD DRIVERS/CONTROLLERS – SELECTION GUIDE

P/N	Pixels	Rows	Columns	Storage RAM [bit]	LCD Voltage [V]	Logic V _{DD} [V]	Power Consumption			Features			Package
							Sleep [µA]	I _{DD} [µA]	I _{LCD} [µA]	General	VLCD factory trimming	Partial Mode	
New EM6110	256	8	32	Dual 8x40	3.0 to 7.0	1.8-5.5V	<1	12	110	2 wires or 3 wires serial interface Digitally Programmable Multiplex Cascadable			Die Gold bumped die TQFP64
	40-160	1,2,3,4	40										
EM6124	904	8	113	25x116	3.0 to 7.0	2.0 to 5.5	0.1	16	65	3 wires serial interface Digitally Programmable Multiplex Cascadable Blank / Set / Inverse	◆		Die Gold bumped die
	1008	9	112										
	1680	16	105										
	1768	17	104										
	2020	20	101										
	2100	21	100										
	2328	24	97										
2400	25	96											
New EM6126	8262	81	102	81x118	4.5 to 11	2.4 to 3.3V	0.1	15	140 50 partial	I2C, 3 wires serial interface Digitally Programmable Multiplex On chip multiplier for VLCD x5, x4, x3, x2 Internal bias resistors, oscillator Temperature compensation of VLCD	◆	◆	Die Gold bumped die
	7670	65	118										

LCD DRIVER/CONTROLLER WITH CHARACTER ROM GENERATOR

P/N	Rows	Columns	Character Line x ch (5x8)	Icons	LCD Voltage [V]	Logic V _{DD} [V]	Power consumption			Features
							Sleep	I _{DD}	I _{LCD} [µA]	
New EM6121	26	80	3x16, 2x24 1x48 +1 scolling	160	3.9 to 9.35	1.8-3.6	<1		75 55 in partial	240 ROM characters 16 RAM characters One line horizontal scrolling 2 rows of Icons Cursor and blink functions No external component Alphanumeric, Chinese, Japanese ROM Characters
	18	80	2x16 1x32 + 2 scolling	160						
	10	80	1x16+ 3 scolling	160						
	2	80	-	160						

TOOLS

P/N	Description
EMDB6110+	Demo kit for EM6110
EMDB6121+	Demo kit for EM6121
EMDB6126+	Demo kit for EM6126

